Agilent 71910A and 71910P
Wide Bandwidth Receiver
Configuration Guide

100 Hz to 26.5 GHz

The Agilent Technologies 71910A/P is a receiver for monitoring signals from 100 Hz to 26.5 GHz. It provides a cost effective combination of search and wide-bandwidth collection capabilities for surveillance and signal monitoring applications. Its flexibility makes it an ideal downconverter in stimulus-response applications.

To search for signals, it sweeps over user-specified spans up to 26.5 GHz wide using bandwidths up to 3 MHz. A wide dynamic range ensures signals of various amplitudes are quickly identified.

Once a signal is located, the receiver is fixed-tuned and the wide IF bandwidths are used for signal collection. (Bandwidths up to 36 MHz are available with microwave preselection, and up to 100 MHz unpreselected). A linear IF signal path provides good signal fidelity with standard outputs of 321.4 MHz IF and linear video. Optional outputs include 70 and 140 MHz IF, analog I/Q, and demodulated FM.

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Ordering Information
Agilent 71910A/P Wide Bandwidth Receiver

Important: Options 001 through 007 are circuit boards. There are only four circuit board slots available. Option 004 consists of two circuit boards. When ordering, be certain the total number of circuit boards does not exceed four.

Option 001	70 MHz IF output **(one board)**
Option 002	140 MHz IF output **(one board)**
Option 004	Analog I/Q output **(two boards)**
Option 005	FM output **(one board)**
Option 007	70 MHz IF channel filters **(one board)** requires Option 001

Option 011	Single mainframe configuration (deletes 70004A, 70902A, 70903A, and 70310A)
Option 012	Add 70310A to Option 011 configuration
Option 013	Add 70902A to Option 011 configuration
Option 014	Add 70903A to Option 011 configuration
Option 016	Add 70620B Option 001 preamplifier (100 kHz to 26.5 GHz)
Option 017	Add 70620B preamplifier (1 GHz to 26.5 GHz)
Option 100	Delete 70902A IF from standard configuration
Option 101	Delete 70903A IF from standard configuration
Option 110	Delete 70310A precision frequency reference from standard configuration
Option 121	Add distribution amplifier to 70310A
Option 122	Delete oven from 70310A
Option 200	Delete 70004A display/mainframe (operation and control over GPIB only, requires additional 70001A mainframe)
Option 205	Substitute 70205A display for 70004A display/mainframe (requires additional 70001A mainframe) (71910A only)
Option 400	Add 400 Hz power line frequency operation to 70001A mainframe; adds 2.3 kg (5 lb) isolation transformer (71910A only)
Option 660	Add 8566B programming manual
Option 810	Rack mount slide kit for both 70001A mainframe and 70004A display/mainframe
Option 0B1	Extra user manual set containing (a) installation/verification, (b) operation, and (c) program language reference
Option 0B3	Service manual and module verification software
Option AX4	Rack flange kit to mount 70001A mainframe and 70004A display/mainframe without handles
Option AXE	Rack flange kit to mount 70001A and 70004A with handles
Option W30	Three-year customer return repair
Option 8ZE	Refurbished 71910A system (71910A only)

Ordering Information
Compatible accessory modules and analysis tools:

Oscilloscopes	70703A; 20 MS/s,
	54610A; 20 MS/s,
Digitizer	70700A; 20 MS/s, 256 K RAM
Power Meter	70100A; –70 to +44 dBm
Attenuator/Switch Driver	70611A
Interface Modules	70612A/C, 70613A/C
Custom Switching	Call your Agilent sales representative
Vector Signal Analyzer	89410A; DC to 10 MHz
Baseband Signal Analyzer	3587S; DC to 4 MHz
RAM Card	82215A (128 k),
	85700A (32 k)
MATE Module	70590A/H69 & H72

Software available from Agilent Technologies
Receiver personality (included when 71910A/P is ordered)
11990A performance verification software

Features and compatibility
70004A system features used: memory card reader, keyboard (for title mode and writing short downloadable programs), direct plot (buffered), direct print, color display

Internal mass storage: 1 Mbyte RAM provided, approximately 750-kbytes available to user, approximately 2.7-kbytes required to store an 800-point trace with its state. System memory is reduced when slave modules are added.

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